



UNITED STATES PATENT AND TRADEMARK OFFICE

DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
BOARD OF PATENT APPEALS AND INTERFERENCES
BOX INTERFERENCE, WASHINGTON, D.C. 20231

Filed by: Judge Sally C. Medley
Telephone: 571-272-9797
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MAILED

AUG 15 2006

PAT. & T.M. OFFICE
BOARD OF PATENT APPEALS
AND INTERFERENCES

Patentees: PEREGO
Application No.: 09/479,375, now Patent No.
6,502,161, granted 12/31/02
Filed: 01/05/00
For: Memory system including a point-to-point
linked memory subsystem

The above-identified application or patent has been forwarded to the Board of Patent Appeals and Interferences because it is adjudged to interfere with another application or patent. An interference has been declared. The interference is designated as No. 105,467.

Notice is hereby given the parties of the requirement of the law for filing in the Patent and Trademark Office a copy of any agreement "in connection with or in contemplation of the termination of the interference." 35 U.S.C. § 135(c).

/Sally C. Medley/
SALLY C. MEDLEY
Administrative Patent Judge

Paper 1

Mail Stop Interference
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Filed 15 August 2006

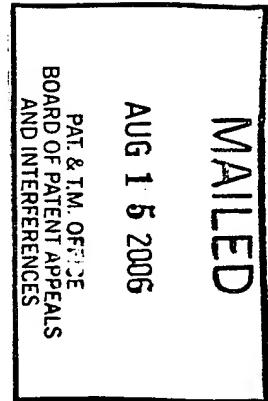
UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE BOARD OF PATENT APPEALS
AND INTERFERENCES

RICHARD E. PEREGO, STEFANOS SIDIROPOULOS
and ELY TSERN
Junior Party
(Patent 6,502,161),

v.

ROBERT ALLEN DREHMEL, KENT HAROLD HASELHORST,
RUSSELL DEAN HOOVER and JAMES ANTHONY MARCELLA
Senior Party
(Application 11/203,652)



Patent Interference No. 105,467 (SCM)
(Technology Center 2100)

DECLARATION - Bd.R. 203(b)¹

Part A. Declaration of interference

An interference is declared (35 U.S.C. § 135(a)) between the above-identified parties. Details of the application(s), patent (if any), reissue application (if any), count(s) and claims designated as corresponding or as not corresponding to the count(s) appear in Parts E and F of this DECLARATION.

¹ "Bd.R. x" may be used as shorthand for "37 C.F.R. § 41.x". 69 Fed. Reg. 49960, 49961 (12 Aug. 2004).

Part B. Judge managing the interference

Administrative Patent Judge Sally Medley has been designated to manage the interference. Bd. R. 104(a).

Part C. Standing order

A Trial Section STANDING ORDER [SO] (Paper 2) accompanies this DECLARATION. The STANDING ORDER applies to this interference.

Part D. Initial conference call

A telephone conference call to discuss the interference is set for **2:00 p.m. on 10 October 2006** (the Board will initiate the call).

No later than **four business days** prior to the conference call, each party shall file and serve (SO ¶¶ 10.1 & 105) a list of the motions (Bd. R. 120; Bd. R. 204; SO ¶¶ 104.2.1, 120 & 204) the party intends to file.

A sample schedule for taking action during the motion phase appears as Form 2 in the STANDING ORDER. Counsel are encouraged to discuss the schedule prior to the conference call and to agree on dates for taking action. A typical motion period lasts approximately eight (8) months. Counsel should be prepared to justify any request for a shorter or longer period.

Part E. Identification and order of the parties

Junior Party

Named inventors: RICHARD E. PEREGO, San Jose, CA
STEFANOS SIDIROPOULOS, Palo Alto, CA
ELY TSERN, Los Altos, CA

Involved Patent: 6,502,161, issued 31 December 2002,
based on application 09/479,375, filed 5 January 2000

Title: Memory system including a point-to-point linked
memory subsystem

Assignee: Rambus Inc.

Senior Party

Named Inventors: ROBERT ALLEN DREHMEL, Goodhue, MN
KENT HAROLD HASELHORST, Byron, MN
RUSSELL DEAN HOOVER, Rochester, MN
JAMES ANTHONY MARCELLA, Rochester, MN

Involved Application: 11/203,652, filed 15 August 2005

Title: Bus architecture employing varying width unidirectional
command bus

Assignee: International Business Machines Corporation

The senior party is assigned exhibit numbers 1001-1999. The junior party is
assigned exhibit numbers 2001-2999. Bd. R. 154(c)(1); SO ¶ 154.2.1. The senior party
is responsible for initiating settlement discussions. SO ¶ 126.1.

Part F. Count and claims of the parties

Count 1

Perego 6,502,161 claim 1

or

Drehmel 11/203,652 claim 11

The claims of the parties are:

Perego: 1-49

Drehmel: 11-59

The claims of the parties which correspond to Count 1 are:

Perego: 1-49

Drehmel: 11-59

The claims of the parties which do not correspond to Count 1, and therefore are not involved in the interference, are:

Perego: none

Drehmel: none

The parties are accorded the following benefit for Count 1:

Perego: none

Drehmel: application 10/747,820², filed 30 December 2003,
application 09/439,068, filed 12 November 1999,
now patent 6,526,469, issued 25 February 2003

²Application 10/747,820 is a reissue of 09/439,068.

Part G. Heading to be used on papers

The following heading must be used on all papers filed in this interference, see SO ¶ 106.1.1:

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RUSSELL DEAN HOOVER and JAMES ANTHONY MARCELLA
Senior Party
(Application 11/203,652).

Patent Interference No. 105,467 (SCM)
(Technology Center 2100)

Part H. Order form for requesting file copies

When requesting copies of files, use of SO Form 4 will greatly expedite processing of the request. Please attach a copy of Parts E and F of this DECLARATION with a hand-drawn circle around the patents and applications for which a copy of a file wrapper is requested.

/Sally C. Medley/
Administrative Patent Judge

Enc:

Copy of STANDING ORDER

Copy U.S. Patent 6,502,161
Copy U.S. Patent 6,526,469
Copy of claims of 11/203,652

cc (via overnight delivery):

Attorney for Perego:

RAMBUS INC.
4440 El Camino Real
Los Altos, CA 94022

Attorney for Drehmel:

FITZPATRICK CELLA (IBM)
30 Rockefeller Plaza
New York, N.Y. 10112-3800

INTERFERENCE DIGEST

Interference No. 105,467

Paper No.

Name: Richard. E. Perego et al.

Serial No.: 09/479,375

Patent No. 6,502,161, granted 12/31/02

Title: Memory system including a point-to-point linked memory subsystem

Filed: 01/05/00

Interference with Drehmel et al.

DECISION ON MOTIONS

Administrative Patent Judge, _____ Dated, _____

FINAL DECISION

Board of Patent Appeals and Interferences, _____ Dated, _____

Court, _____ Dated, _____

REMARKS

This should be placed in each application or patent involved in interference in addition to the interference letters.